

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein

Title: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE  
UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD  
THEREFOR

Application No.: 09/990,894

Filed:

November 16, 2001

Examiner: Connie C. Yoha

Group Art Unit:

2818

Atty. Docket No.: 023-0013

August 7, 2003

Mail Stop Non Fee Amendment  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450**RESPONSE TO NON-FINAL OFFICE ACTION**

This paper is responsive to a Non-Final Office Action mailed May 8, 2003, having a shortened statutory period for response set to expire August 8, 2003. In light of the Amendments and/or Remarks herein, further examination is requested.

Please amend the above-identified application as follows:

OFFICIAL

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**FACSIMILE MESSAGE TRANSMITTAL**

|                     |  |                 |                                 |
|---------------------|--|-----------------|---------------------------------|
| <b>TO:</b>          | Examiner, Connie C. Yoha   | <b>PHONE #:</b> |                                 |
| <b>FROM:</b>        | Andrew C. Graham<br>Reg. No. 36,531  | <b>DATE:</b>    | August 7, 2003                  |
| <b>SUBJECT:</b>     | U.S. Application entitled "INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD THEREFOR"<br>Application No.: 09/990,894<br>Filing Date: November 16, 2001<br>Group Art Unit: 2818<br><u>Action Taken: Response to Non-Final Office Action mailed May 8, 2003</u> |                 |                                 |
| <b>YOUR REF:</b>    | 09/990,894   | <b>OUR REF:</b> | 023-0013                        |
| <b>FACSIMILE #:</b> | 703-872-9318 (TC 2800 BF)  | <b>PAGES:</b>   | 20 (including this transmittal) |


**ATTACHED HERETO:**

- (1) Response to Non-Final Office Action (19 pages)

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**Andrew C. Graham**

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